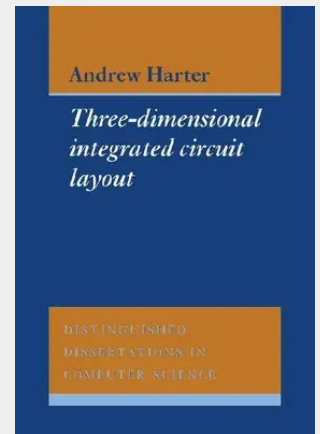


Harter

Three-Dimensional Int Circuit L

The theory and design of integrated circuits has moved forward in leaps and bounds in recent years. This thesis concentrates upon the design of three-dimensional, rather than the traditional two-dimensional, circuits. The theory behind such circuits is presented in detail, together with experimental results. Winner of the Distinguished Dissertation in Computer Science award, this work will prove invaluable to both designers and hardware engineers involved in the development of practical three-dimensional integrated circuits.

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