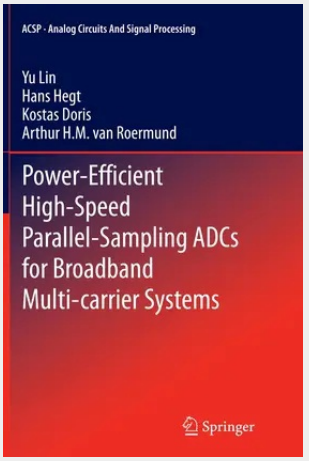
Lin / Hegt / Doris

Power-Efficient High-Speed Parallel-Sampling ADCs for Broadband Multi-carrier Systems

This book addresses the challenges of designing high performance analog-to-digital converters (ADCs) based on the “smart data converters” concept, which implies context awareness, on-chip intelligence and adaptation. Readers will learn to exploit various information either a-priori or a-posteriori (obtained from devices, signals, applications or the ambient situations, etc.) for circuit and architecture optimization during the design phase or adaptation during operation, to enhance data converters performance, flexibility, robustness and power-efficiency. The authors focus on exploiting the a-priori knowledge of the system/application to develop enhancement techniques for ADCs, with particular emphasis on improving the power efficiency of high-speed and high-resolution ADCs for broadband multi-carrier systems.

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106,99 €
99,99 € (zzgl. MwSt.)

Lieferfrist: bis zu 10 Tage

Artikelnummer: 9783319368917
Medium: Buch
ISBN: 978-3-319-36891-7
Verlag: Springer
Erscheinungstermin: 09.10.2016
Sprache(n): Englisch
Auflage: Softcover Nachdruck of the original 1. Auflage 2015
Serie: Analog Circuits and Signal Processing
Produktform: Kartoniert
Gewicht: 2058 g
Seiten: 115
Format (B x H): 155 x 235 mm

Kundenservice Verlag Dr. Otto Schmidt KG
Neumannstraße 10, 40235 Düsseldorf | kundenservice@otto-schmidt.de | 0221-93738-997 (Inland)