

Reversible Computation

This book constitutes the refereed proceedings of the 9th International Conference on Reversible Computation, RC 2017, held in Kolkata, India, in July 2017.

The 13 full and 5 short papers included in this volume together with one invited paper were carefully reviewed and selected from 47 submissions. The papers are organized in the following topical sections: foundations; reversible circuit synthesis; reversible circuit optimization; testing and fault tolerance; and quantum circuits.

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53,49 €

49,99 € (zzgl. MwSt.)

Lieferfrist: bis zu 10 Tage

Artikelnummer: 9783319599359

Medium: Buch

ISBN: 978-3-319-59935-9

Verlag: Springer

Erscheinungstermin: 25.05.2017

Sprache(n): Englisch

Auflage: 1. Auflage 2017

Serie: Programming and Software Engineering

Produktform: Kartoniert

Gewicht: 4161 g

Seiten: 255

Format (B x H): 155 x 235 mm

