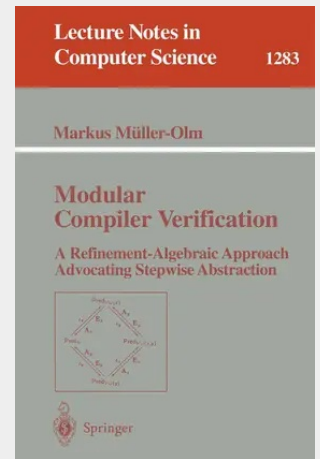


Modular Compiler Verification

A Refinement-Algebraic Approach Advocating Stepwise Abstraction

This book presents the verified design of a code generator translating a prototypic real-time programming language to an actual microprocessor, the Inmos Transputer. Unlike most other work on compiler verification, and with particular emphasis on modularity, it systematically covers correctness of translation down to actual machine code, a necessity in the area of safety-critical systems. The formal framework provided as well as the novel proof-engineering ideas incorporated in the verified code generator are also of relevance for software design in general.

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53,49 €

49,99 € (zzgl. MwSt.)

Lieferfrist: bis zu 10 Tage

Artikelnummer: 9783540634065

Medium: Buch

ISBN: 978-3-540-63406-5

Verlag: Springer Berlin Heidelberg

Erscheinungstermin: 06.08.1997

Sprache(n): Englisch

Auflage: 1997

Serie: Lecture Notes in Computer Science

Produktform: Kartoniert

Gewicht: 423 g

Seiten: 260

Format (B x H): 155 x 235 mm

