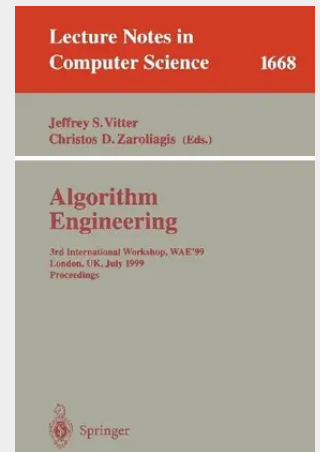


Algorithm Engineering

This work considers practical parallel list-ranking algorithms. The model for which programs are written is a single-program multiple-data (SPMD) "programming model". This model is designated as a programmer's model for a fine-grained computation framework called Explicit Multi-Threading (XMT), which was introduced in [VDBN98]; the XMT framework covers the spectrum from algorithms through architecture to implementation; it is meant to provide a platform for faster single-task completion time by way of instruction-level parallelism (ILP). The performance of XMT programs is evaluated as follows: the performance of a matching optimized XMT assembly code is measured within an XMT execution model. (We use in the current paper the so-called Spawn-MT programming model - the easier to implement among the two programming models presented in [VDBN98]). The XMT approach deviates from the standard PRAM approach by incorporating reduced synchrony and departing from the lock-step structure in its so-called asynchronous mode. Our envisioned platform uses an extension to a standard serial instruction set. This extension efficiently implements PRAM-style algorithms using explicit multi-threaded ILP, which allows considerably more fine-grained parallelism than the previously studied parallel computing implementation platforms/models. The list ranking problem was the first problem considered as we examined and refined many of the concepts in the XMT framework. The problem arises in parallel algorithms on lists, trees and graphs and is considered a fundamental problem in the theory of parallel algorithms. Experimental results are presented.

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