

VLSI Engineering

Beyond Software Engineering

Future directions for VLSI and software engineering.- An object-oriented, capability-based architecture.- Super freedom simulator PAX- Prolog interpreter and its parallel extension.- Putting inner loops automatically in silicon.- Hardware algorithms for VLSI systems.- Graph-based design specification of parallel computation.- Experience with specification and verification of hardware using PROLOG.- VLSI verification and correction.- Automated logic synthesis.- A network for parallel searching.- A hardware file system for database storage.- Top-down VLSI design experiments on a picture database computer.- Advances in digital image processing for document reproduction.

Future directions for VLSI and software engineering.- An object-oriented, capability-based architecture.- Super freedom simulator PAX- Prolog interpreter and its parallel extension.- Putting inner loops automatically in silicon.- Hardware algorithms for VLSI systems.- Graph-based design specification of parallel computation.- Experience with specification and verification of hardware using PROLOG.- VLSI verification and correction.- Automated logic synthesis.- A network for parallel searching.- A hardware file system for database storage.- Top-down VLSI design experiments on a picture database computer.- Advances in digital image processing for document reproduction.



37,44 €

34,99 € (zzgl. MwSt.)

Lieferfrist: bis zu 10 Tage

Artikelnummer: 9784431700029

Medium: Buch

ISBN: 978-4-431-70002-9

Verlag: Springer

Erscheinungstermin: 01.05.1984

Sprache(n): Englisch

Auflage: 1. Auflage 1984

Serie: Lecture Notes in Computer Science

Produktform: Kartoniert

Gewicht: 1000 g

Seiten: 308

Format (B x H): 155 x 235 mm

